Docket No.: SON-3210

JC20 Rec'd PCT/PTO 21 SEP 2009

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of: Shigeru Arisawa, et al

Application No.: Not Yet Assigned

Confirmation No.:

Filed: September 21, 2005

Art Unit: N/A

For: PLL CIRCUIT, DEMODULATING CIRCUIT, IC CARD, AND IC CARD PROCESSING

APPARATUS

Examiner: Not Yet Assigned

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir

In accordance with the requirements of 37 CFR §§ 1.56 and MPEP 609, applicant through his attorneys and representatives, hereby brings to the attention of the Examiner the references noted on the attached Form PTO-1449, copies of which are enclosed.

The references are cited in the International Search Report of applicant's corresponding PCT Application No. PCT/JP2005/002161, a copy of which is enclosed.

It is requested that the Examiner review the references noted and make such consideration of record.

Respectfully submitted,

Dated: September 21, 2005

Ronald P. Kananen Reg. No. 24,104

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STATEMENT BY APPLICANT				First Named Inventor	Shigeru Arisawa		
_				Art Unit	N/A- 2611		
(Use as many sheets as necessary)				Examiner Name	Not Yet Assigned Young Tse		
Sheet	1	of	1	Attomey Docket Number	SON-3210		

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ² (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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	FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code ³ -Number Hind Code ⁵ (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	70	
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7Y.1./	CA	International Search Report dated May 17, 2005			

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Examiner Signature	/Young Tse/	Date Considered	02/14/2009